

WHAT IS CLAIMED IS:

1. A PCSB assembly comprising:
  - a PCSB;
  - a first plurality of LVD SCSI bus signal trace pairs formed in said PCSB; and
  - a second plurality of LVD SCSI bus signal trace pairs formed in said PCSB and positioned next adjacent one another for the entire length thereof comprising a RESET signal trace pair, a SELECT signal trace pair and a BUSY signal trace pair.
2. The PCSB assembly of claim 1 wherein the minimum spacing of any signal trace pair in said second plurality of signal trace pairs from any signal trace pair in said first plurality of signal trace pairs is about 0.015 in.
3. The PCSB assembly of claim 2 wherein the minimum spacing between any two adjacent signal trace pairs of said second plurality of LVD SCSI bus signal trace pairs is about 0.008 in.
4. The PCSB assembly of claim 3 wherein the minimum internal spacing between traces of a signal trace pair of any of said second plurality of LVD SCSI signal trace pairs is about 0.006 in.
5. The PCSB assembly of claim 1 wherein said PCSB comprises a first exterior surface layer and a second exterior surface layer opposite said first exterior surface layer and wherein

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said first plurality of LVD SCSI bus signal trace pairs are positioned in said first surface layer and wherein said second plurality of LVD SCSI bus signal trace pairs are positioned at least partially in said second exterior surface pair.

6. The PCSB assembly of claim 5, wherein said second plurality of signal trace pairs are positioned partially in said first exterior surface layer and partially in said second exterior surface layer.

7. The PCSB assembly of claim 5, wherein said second plurality of signal trace pairs are positioned entirely in said second exterior surface layer.

8. The PCSB assembly of claim 2 wherein said PCSB comprises a first exterior surface layer and a second exterior surface layer opposite said first exterior surface layer and wherein said first plurality of LVD SCSI bus signal trace pairs are positioned in said first surface layer and wherein said second plurality of LVD SCSI bus signal trace pairs are positioned at least partially in said second exterior surface pair.

9. The PCSB assembly of claim 8, wherein said second plurality of signal trace pairs are positioned partially in said first exterior surface layer and partially in said second exterior surface layer.

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10. The PCSB assembly of claim 9, wherein said second plurality of signal trace pairs are positioned entirely in said second exterior surface layer.

11. A PCSB comprising:  
a first surface layer comprising a plurality of LVD SCSI bus signal trace pairs; and  
a second surface layer opposite said first surface layer comprising at least a portion of at least one signal trace pair selected from the group of: a LVD SCSI bus RESET signal trace pair; a LVD SCSI bus SELECT signal trace pair and a LVD SCSI bus BUSY signal trace pair; and excluding all LVD SCSI bus signal trace pairs other than those in said group.

12. A method of forming a PCSB comprising:  
forming a plurality of LVD SCSI bus signal trace pairs in a first exterior layer of the PCSB; and  
forming at least a portion of at least one of a RESET, SELECT and BUSY signal trace pair of the LVD SCSI bus in a second exterior surface layer of the PCSB.

13. A method of forming a PCSB comprising:  
forming a plurality of LVD SCSI bus signal trace pairs in at least one exterior layer of the PCSB including forming RESET, SELECT and BUSY signal trace pairs; and  
maintaining the RESET, SELECT, and BUSY signal trace pairs in next adjacent relationship for their entire length whereby no other LVD SCSI bus signal trace pairs are interposed between

adjacent ones of the RESET, SELECT, and BUSY signal trace pairs.

14. The method of claim 13 further comprising maintaining a spacing of at least about 0.015 in. between any of the RESET, SELECT, and BUSY signal trace pairs and the closest other LVD SCSI bus signal trace pair which is not one of the RESET, SELECT, and BUSY signal trace pairs.

15. The method of claim 14 further comprising maintaining a spacing of at least about 0.008 in. between adjacent ones of the RESET, SELECT, and BUSY signal trace pairs.

16. The method of claim 13 wherein forming a plurality of LVD SCSI bus signal trace pairs in at least one exterior layer of the PCSB including forming RESET, SELECT and BUSY signal trace pairs comprises forming at least a portion of the RESET, SELECT and BUSY signal trace pairs in a different exterior surface layer from the one where the other of LVD SCSI bus signal trace pairs are formed.

17. The method of claim 14 wherein forming a plurality of LVD SCSI bus signal trace pairs in at least one exterior layer of the PCSB including forming RESET, SELECT and BUSY signal trace pairs comprises forming at least a portion of the RESET, SELECT and BUSY signal trace pairs in a different exterior surface layer from the one where the other of LVD SCSI bus signal trace pairs are formed.

18. The method of claim 15 wherein forming a plurality of LVD SCSI bus signal trace pairs in at least one exterior layer of the PCSB including forming RESET, SELECT and BUSY signal trace pairs comprises forming at least a portion of the RESET, SELECT and BUSY signal trace pairs in a different exterior surface layer from the one where the other of LVD SCSI bus signal trace pairs are formed.

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